

## IN THE SPECIFICATION

Please replace paragraph [0038] in the specification with the following paragraph:

[0038] As will be discussed in more detail below, the example processors 110 support launch AC instructions that authenticated the AC module 190 prior to execution. Accordingly, the AC module 190 may comprise information to support authenticity determinations by the processors 110. For example, the signature 240 may comprise a digest value 242. The digest value 242 may be generated by passing the AC module 190 through a hashing algorithm (e.g. SHA-1 or MD5) or some other algorithm. The signature 240 may also be encrypted to prevent alteration of the digest value 242 via an encryption algorithm (e.g. DES, 3DES, AES, and/or RSA algorithms). In example embodiment, the signature 240 is RSA-encrypted with the private key that corresponds to a public key of the processor key 116, the chipset key ~~120~~ 122, and/or platform key 152.

Please replace paragraph [0051] in the specification with the following paragraph:

[0051] Otherwise, the processor 110 in block ~~414~~ 412 may update event processing to support launching the AC module 190. In an example embodiment of the ENTERAC instruction, the processor 110 masks processing of the INTR, NMI, SMI, INIT, and A20M events. Other launch AC instructions and/or associated operands may specify masking fewer, additional, and/or different events. Further, other launch AC instructions and/or associated operands may explicitly specify the events to be masked and the events to be unmasked. Alternatively, other embodiments may avoid masking events by causing the computing device 100 to execute trusted code such as, for example, event handlers of the AC module 190 in response to such events.